

Notice of References Cited	Application/Control No. 10/004,553	Applicant(s)/Patent Under Reexamination CADDEN ET AL.	
	Examiner Sue Lao	Art Unit 2126	Page 1 of 1

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	B	US-5,109,511	04-1992	Nitta et al.	718/104
	C	US-5,307,487	04-1994	Tavares et al.	707/8
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	J	US-6,799,317	09-2004	Heywood et al.	719/313
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM TDB, "Compare and Swap Implementation of Task Logic", July 1973, pp. 474-476.
	V	J. Valois, "Lock Free Linked List Using Compare-and-Swap", ACM, 1995, pp.214-222.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.